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BY TELEFAX 703-746-9195
DOCKET NO.: 4858

UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE MATTER OF THE APPLICATION FOR PATENT

OF: Kazuhiro FUJIKAWA et al.

ART UNIT: 2812

SERIAL NO.: 10/528,440

CONF. NO.: 4308

FILED: March 18, 2005

FOR: METHOD OF FABRICATING SEMICONDUCTOR DEVICE

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September 28, 2005

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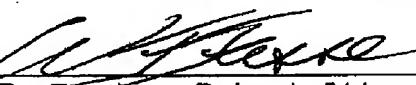
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2) The requested correction is supported by the inventors' Declaration, the specification and our transmittal letter filed on March 18, 2005.

3) Please issue a corrected Filing Receipt.

Respectfully submitted,
Kazuhiro Fujikawa et al.-Applicant

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By 
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I hereby certify that this correspondence with all indicated enclosures is being transmitted by telefax to 703-746-9195 on the date indicated below, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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APPL NO.	FILING OR 371 (C) DATE	ART UNIT	FL. FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/528,440	03/18/2005	2812	1050	4858	3	23	2

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CONFIRMATION NO. 4308
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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 021553.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/05649 04/20/2004

Foreign Applications

JAPAN 2003-122240 04/25/2003
 JAPAN 200314462 05/22/2003

Projected Publication Date: 12/15/2005

Non-Publication Request: No

Early Publication Request: No

X Title

of fabricating
 Method for manufacturing semiconductor device

X